



FORM PTO-1449

THIRD SUPPLEMENTAL  
INFORMATION DISCLOSURE STATEMENTATTY. DOCKET NO.  
1778.1730000APPLICATION NO.  
09/702,112FIRST NAMED INVENTOR  
Michael Gottlieb JensenFILING DATE  
October 30, 2000ART UNIT  
2183

## U.S. PATENT DOCUMENTS

| EXAMINER<br>INITIAL |     | DOCUMENT<br>NUMBER | DATE    | NAME                   | CLASS | SUB-CLASS | FILING DATE |
|---------------------|-----|--------------------|---------|------------------------|-------|-----------|-------------|
| RLE                 | AA1 | 4,068,303          | 01/1978 | Morita                 | —     | —         | —           |
| RLE                 | AB1 | 4,814,975          | 03/1989 | Hirosawa <i>et al.</i> | —     | —         | —           |
| RLE                 | AC1 | 5,517,664          | 05/1996 | Watanabe <i>et al.</i> | —     | —         | —           |
| RLE                 | AD1 | 5,632,024          | 05/1997 | Yajima <i>et al.</i>   | —     | —         | —           |
| RLE                 | AE1 | 6,219,774 B1       | 04/2001 | Hammond <i>et al.</i>  | —     | —         | 03/25/1998  |
|                     | AF1 |                    |         |                        |       |           |             |
|                     | AG1 |                    |         |                        |       |           |             |
|                     | AH1 |                    |         |                        |       |           |             |
|                     | AI1 |                    |         |                        |       |           |             |
|                     | AJ1 |                    |         |                        |       |           |             |
|                     | AK1 |                    |         |                        |       |           |             |

RECEIVED

APR 19 2004

Technology Center 2100

## FOREIGN PATENT DOCUMENTS

| EXAMINER<br>INITIAL |     | DOCUMENT<br>NUMBER | DATE | COUNTRY | CLASS | SUB-CLASS | TRANSLATION |
|---------------------|-----|--------------------|------|---------|-------|-----------|-------------|
|                     | AL1 |                    |      |         |       |           | Yes<br>No   |
|                     | AM1 |                    |      |         |       |           | Yes<br>No   |
|                     | AN1 |                    |      |         |       |           | Yes<br>No   |
|                     | AO1 |                    |      |         |       |           | Yes<br>No   |
|                     | AP1 |                    |      |         |       |           | Yes<br>No   |

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

|     |    |   |  |
|-----|----|---|--|
| RLE | AR | 1 | Turley, J., "LSI's TinyRisc Core Shrinks Code Size: Code-Compaction Technique Squeezes MIPS Instructions Into 16 Bits," Microprocessor Report, Microdesign Resources, October 28, 1996, pp. 40-43. |
| RLE | AS | 1 | Sweetman, D., <i>See MIPS Run</i> , Morgan Kaufmann Publishers, Inc., ISBN 1-55860-410-3, 1999, pp. vii-xiv and 423-425.   |
| RLE | AT | 1 | <i>User's Manual VR4121™ 64/32-Bit Microprocessor</i> , NEC Corporation, 1998, pp. 1-19 and 103-131.   |

EXAMINER

Richard Ellis

DATE CONSIDERED

5/18/2004

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.